

STEVEN E. TURNER

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KALEO CONSULTING EMPLOYMENT EXPERIENCE:

TELECOMMUNICATIONS AND FINANCIAL CONSULTANT (Jan 1997-Present)

- ?? Provide expert testimony on technical issues surrounding the unbundling and interconnection to incumbent Local Exchange Company (ILEC) networks. The testimony includes analysis of ILEC unbundling and interconnection per the Telecommunications Act of 1996 (Section 271) as well as other technical issues of local market entry. Further, the testimony includes evaluating and conducting unbundled element and interconnection cost studies.
- ?? Provide expert testimony on the level and extent of facilities-based competition in the local market place. This testimony which quantitatively and economically evaluates the extent of competition results in an assessment of ILEC compliance with Section 271 proceedings.
- ?? Develop models to aid companies in developing market entry plans for the local telecommunications market. This assistance includes evaluating what market entry alternatives as well as which geographies provide the best profit opportunities for the new entrant.

AT&T EMPLOYMENT EXPERIENCE:

DISTRICT MANAGER - CONNECTIVITY NETWORK PLANNING - LI&AM (Feb 1996-Dec 1996)

- ?? Managed the development of AT&T's Infrastructure Plans of Record for the Southwest region. These plans entailed defining the right mix of built and leased infrastructure to meet AT&T's local offer needs at the least cost.
- ?? Managed AT&T's dedicated access inventory in the Southwest region. This effort involved identifying the optimum supplier(s) in each market for AT&T's access needs to meet both financial and strategic objectives.

MANAGER - STRATEGIC ACCESS PLANNING - Access Strategic Planning (Nov 1994-Feb 1996)

- ?? Managed the development of strategic models to analyze alternatives for entering the local market. These models considered various technologies for entering local that would optimize the contribution to AT&T from a revenue, expense, and capital perspective.

RE-ENGINEERING MANAGER - Network Operations (Jul 1994-Oct 1994)

- ?? Directed a CCS-NSD management-union team in re-engineering the engineering, provisioning, and maintaining of the Operator Services network. Delivered a re-engineered process that reduced operational expense significantly while mitigating the impacts on customers and employees.

PROJECT MANAGER/SYSTEM ENGINEER - CCS Centralized Test Center (Jan 1992-Jun 1994)

- ?? Coordinated implementation plans and system development for new services and network elements in the Common Channel Signaling (CCS) Network. The planning scope included provisioning, monitoring, and maintaining the T1.5 facilities for the CCS signaling circuits.
- ?? Acquired funding (development, capital, and head count) through writing and defending business cases in support of projects for new services or network elements in the CCS Network. Upon approval, coordinated the implementation of system development and capital projects affecting the CCS Centralized Test Center.

AT&T EMPLOYMENT EXPERIENCE (cont.):

DEPARTMENTAL QUALITY MANAGER - Network Operations (Jan 1990-Jan 1992)

- ?? Developed the Network Operations Quality Management System and implemented it into an organization of 5000 people. Implementation required gaining organizational support for staffing and training 40 Quality Specialists and managing their efforts in transferring the quality technology into Network Operations.

OPERATIONS SUPERVISOR - Regional Network Service Center (Nov 1988-Dec 1989)

- ?? Managed the Regional Network Service Center serving AT&T customers in the Southeastern United States through correcting their service troubles. Responsibilities included leading a team of 20 associates who responded to over 2000 customer troubles per month and escalating with Local Exchange Companies to remove barriers to trouble resolution.

4ESS SWITCH ENGINEER - Network Engineering Services (Dec 1987-Nov 1988)

- ?? Identified current levels of asset utilization, analyzed future needs, and developed a capital budget to purchase and provision the necessary equipment to efficiently meet customer needs. Managed the implementation of over \$10M in capital projects.

GENERAL ELECTRIC EMPLOYMENT EXPERIENCE:

RESEARCH AND DESIGN ENGINEER - Simulation and Control Systems (Jun 1986-Dec 1987)

- ?? Designed and developed a major sub-system for a high-speed graphics simulator supporting both defense and commercial customers.
- ?? Designed and developed a Very Large-Scale Integrated (VLSI) Chip with over 80,000 transistors used in the video display sub-system for the high-speed graphics simulator.

ACHIEVEMENTS:

- ?? Developed the strategic planning system used throughout AT&T Connectivity Planning that identifies the mix of connectivity options (Wireless, CATV, LEC) that AT&T should implement within a market. This model is being used to determine AT&T's local market entry strategy for the entire country.
- ?? Re-engineered the Operator Services operations processes through a collaborative effort of management and union employees yielding \$19.9 million in operational expense savings annually while making the new organization more customer responsive.
- ?? Planned and implemented a modification to the CCS Network data collection architecture resulting in operational expense savings of \$7.3 million per year.
- ?? Significantly advanced the implementation of Total Quality Management in Network Operations through the Quality Specialist strategy initiative begun in 1990.
- ?? Completed development of a Win Back Program for non-AT&T customers who called the Regional Network Service Center in error. This program generated over \$1.6 million in new revenue for AT&T in 1989.
- ?? Designed and developed a Management Information System enabling the measurement of asset utilization in switching equipment at any point in time. The use of the information provided with this system and the resulting changes in engineering practices reduced Network Operations under-utilized switching assets by approximately \$250 million.
- ?? Re-engineered the installation process for switching equipment resulting in a 70% reduction in the installation interval.
- ?? Designed and developed the largest VLSI chip with General Electric at that time in only five months.

EDUCATION:

August 1990: **Masters of Business Administration Degree - Finance**
Georgia State University
Atlanta, Georgia

December 1986: **Bachelor of Science Degree - Electrical Engineering**
Auburn University
Auburn, Alabama